

FIG. 1 (Prior Art)

FIG. 2 (Prior Art)

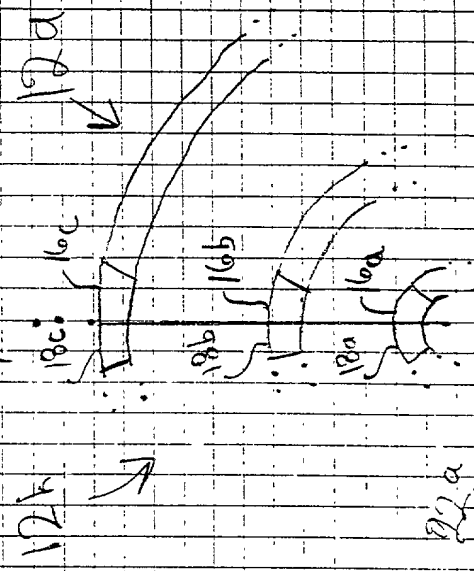


FIG. 3 (Prior Art)

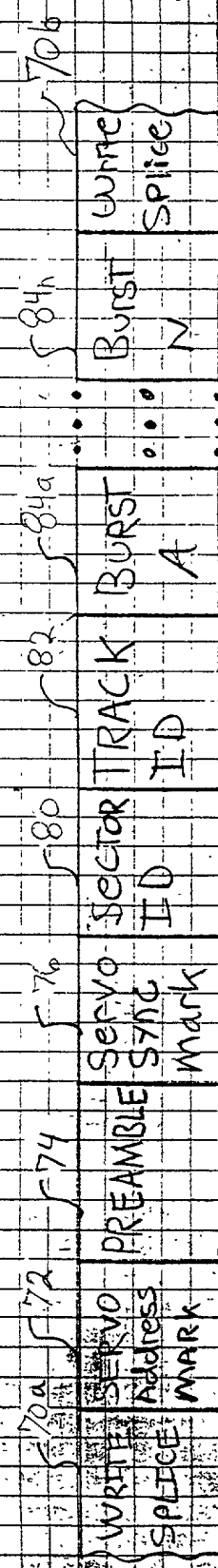


FIG. 6



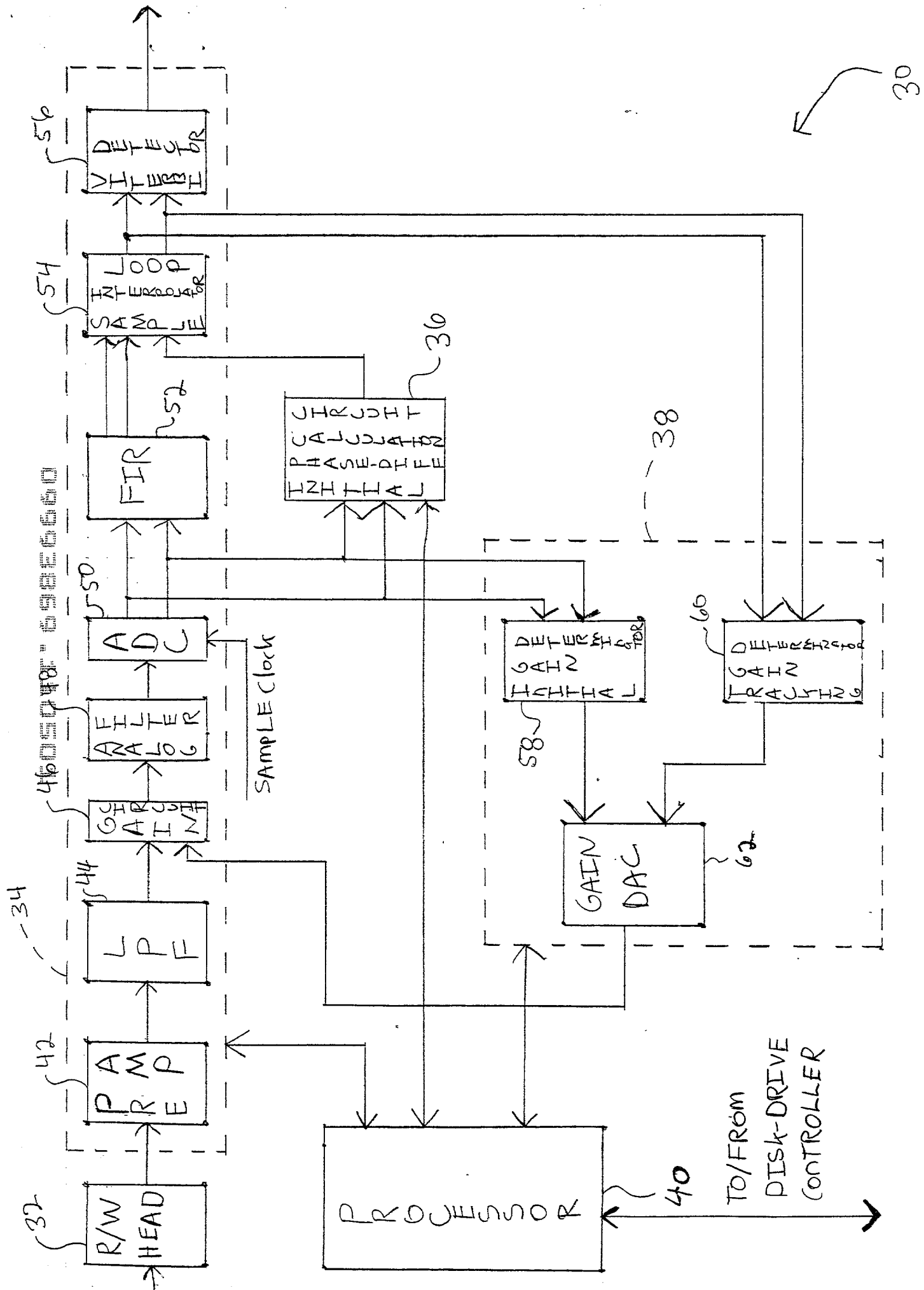
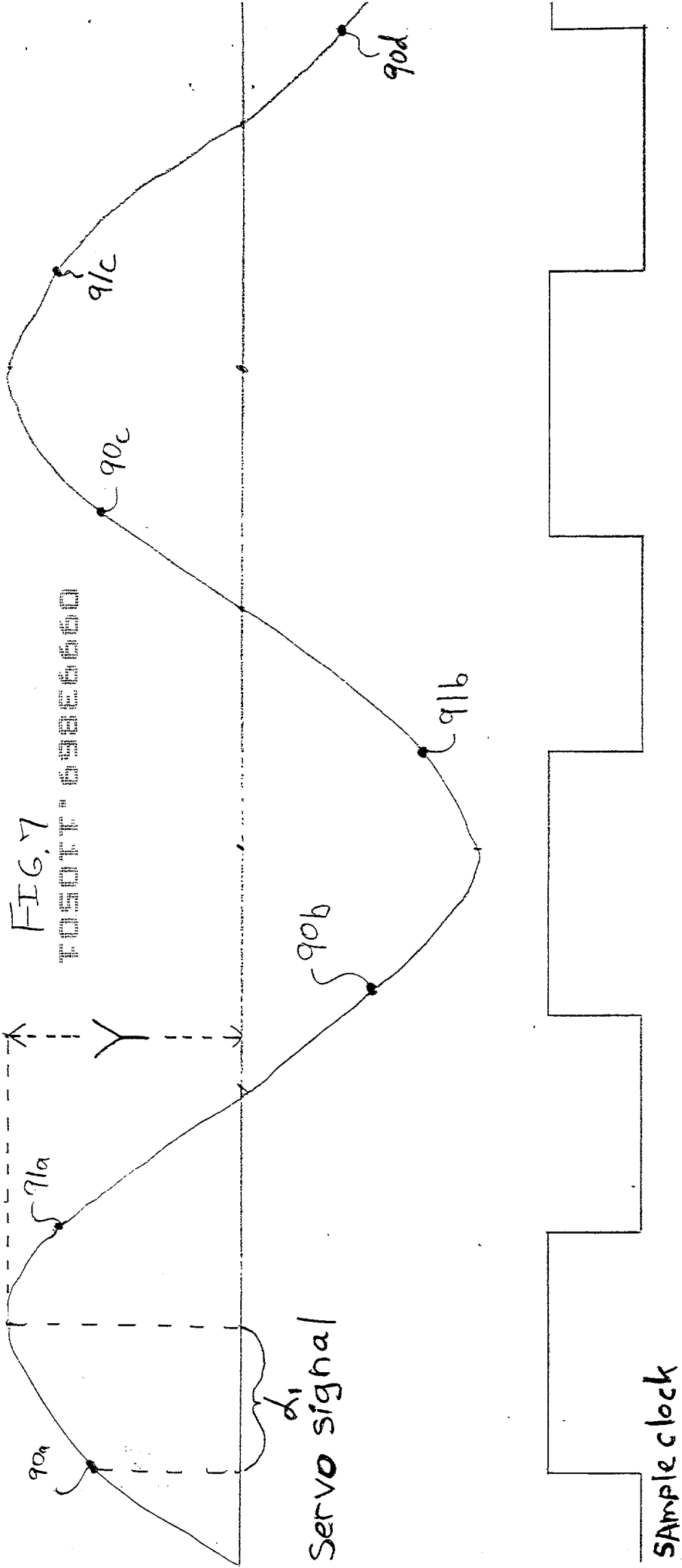


FIG. 5



K — PREAMBLE — \* — SERVO SYNCHRONIZATION MARK — → Sector ID  
 001100110011000000001100000011000011XXXXXX

FIG. 14

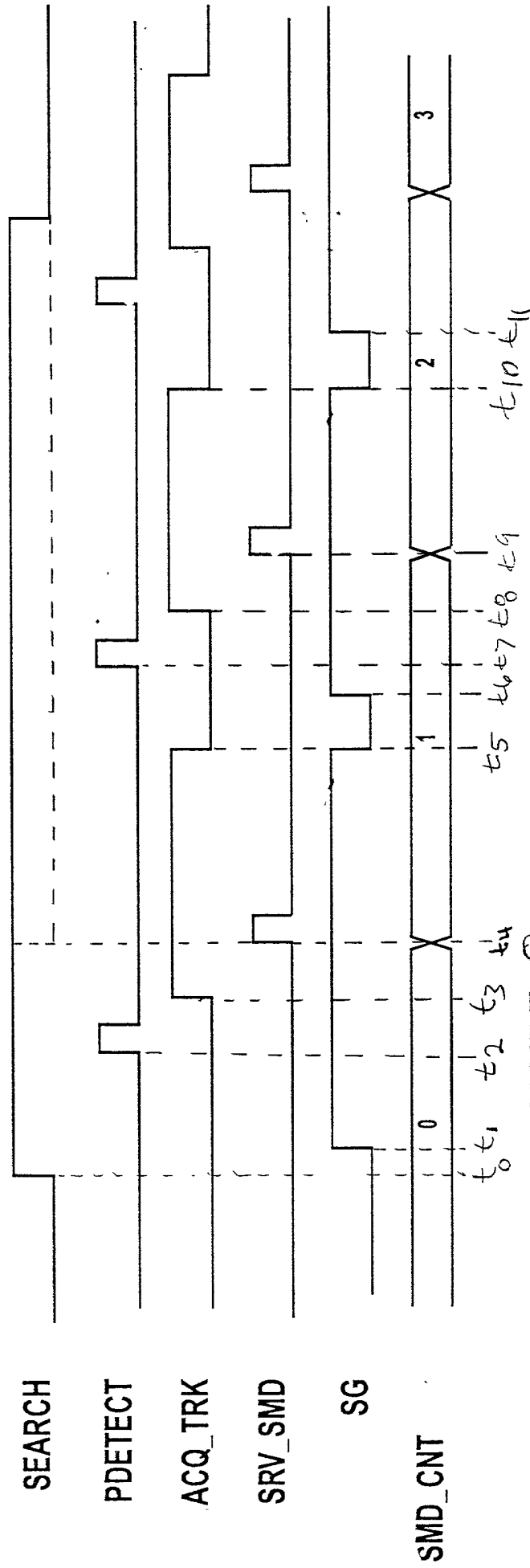


FIGURE 8

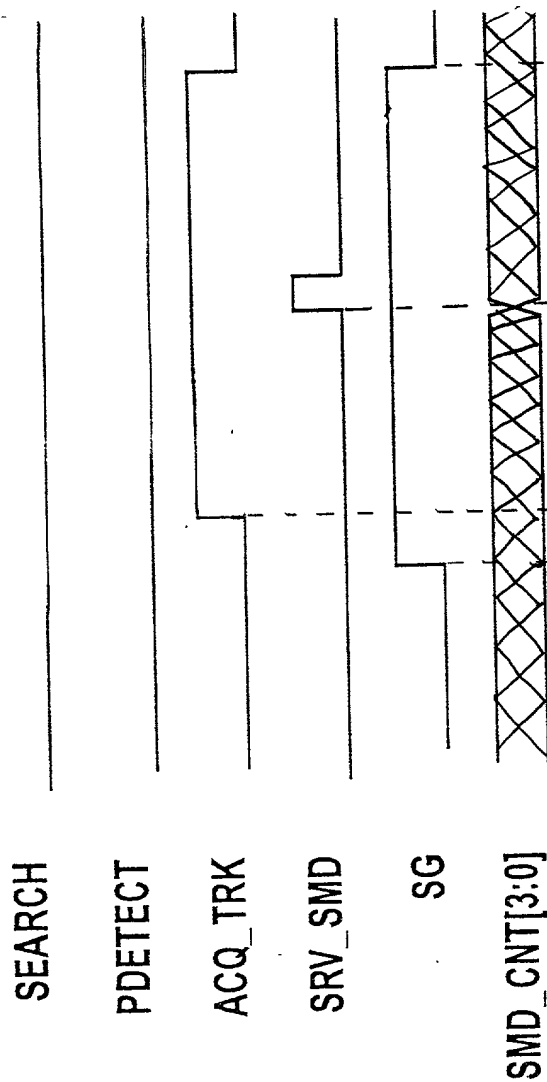


FIGURE 9

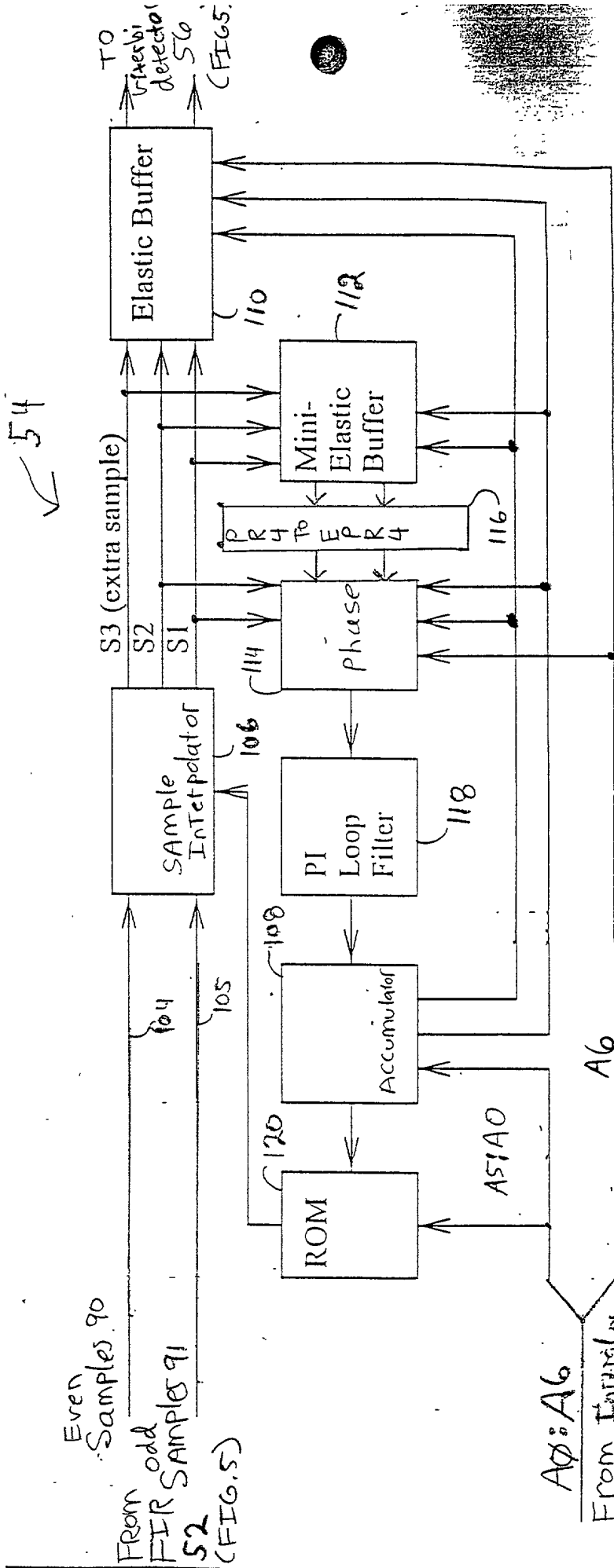


Figure 10

A6: A6  
From Initial phase  
difference  
calculation  
circuit 36 (FIG. 5)

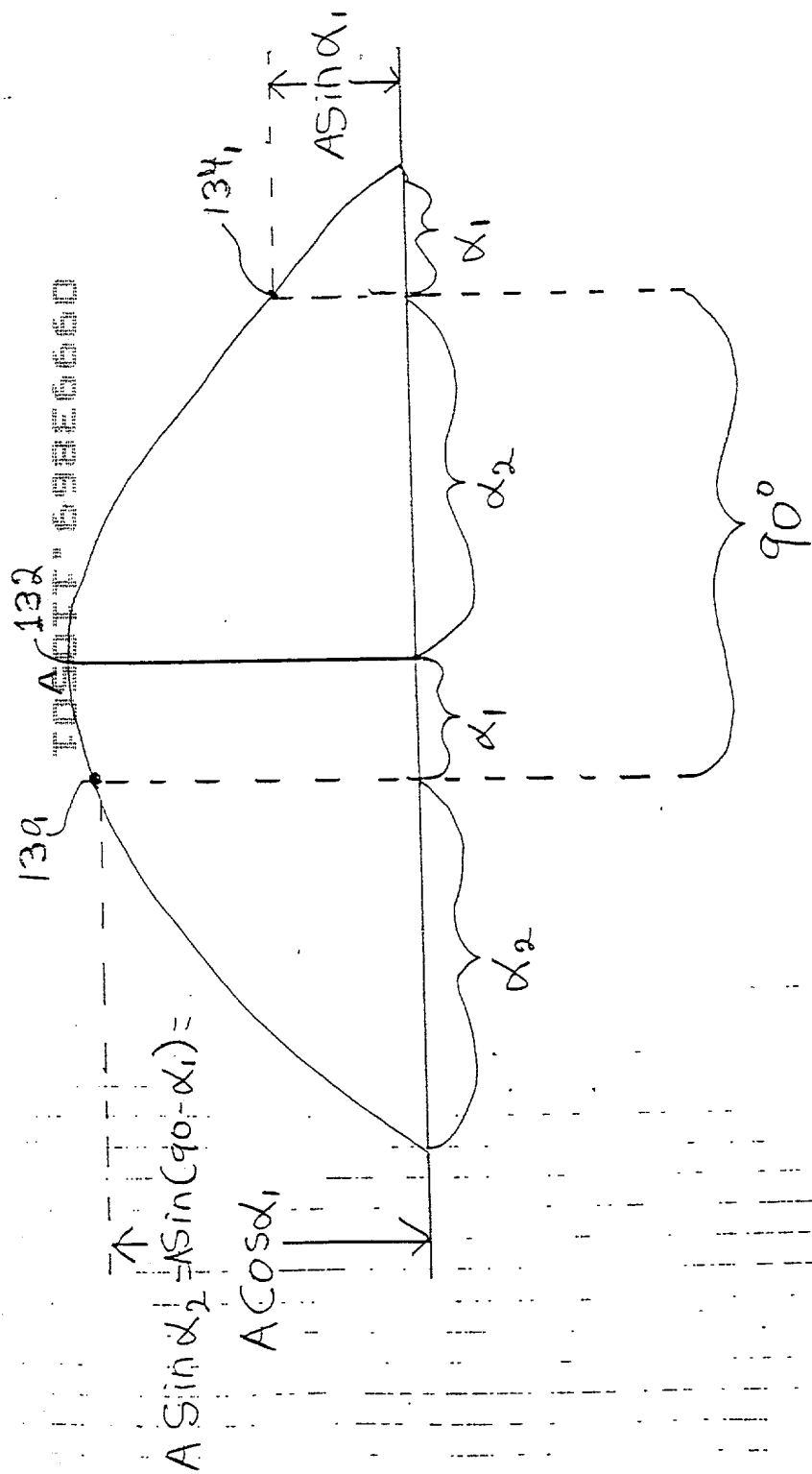


Figure : 11



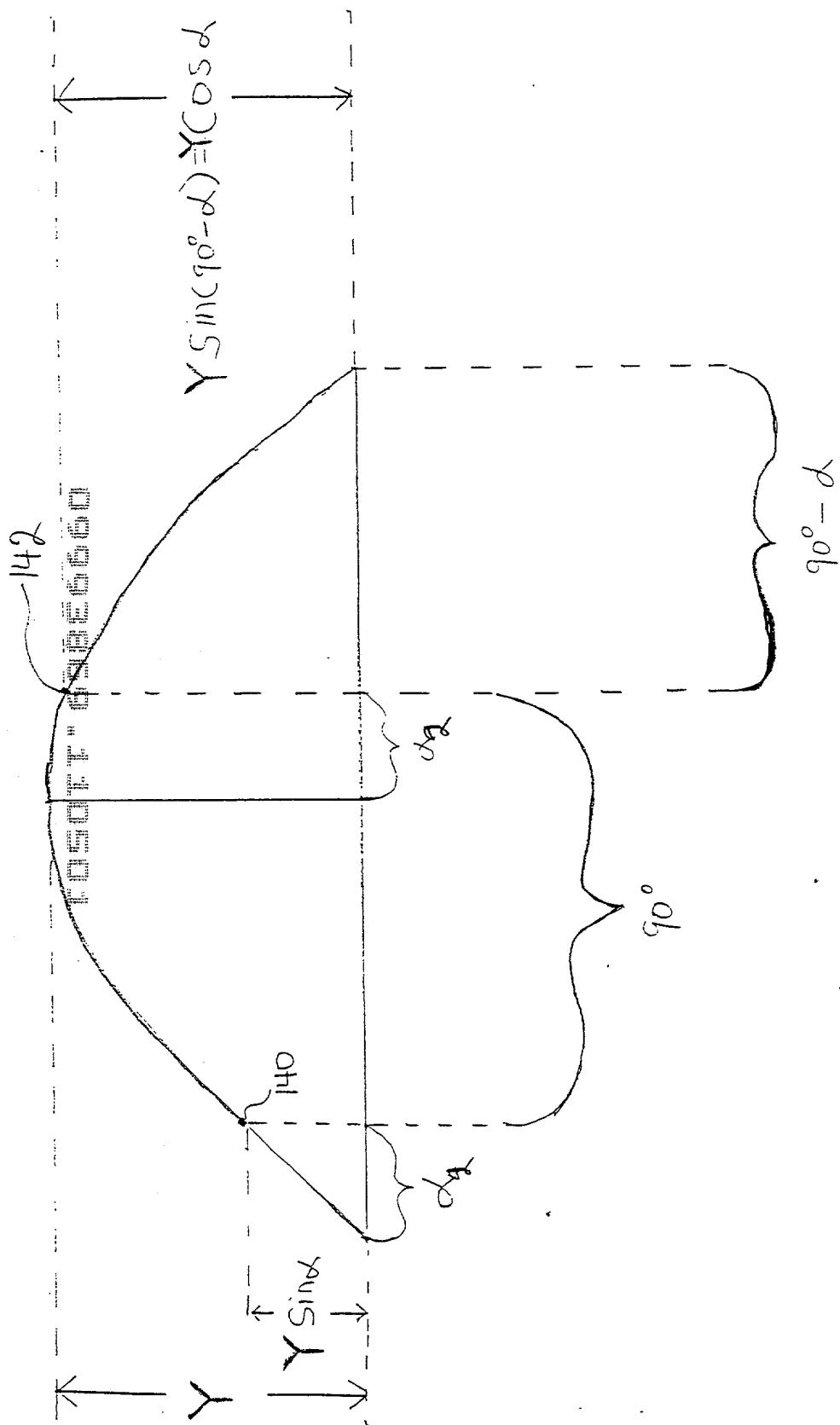
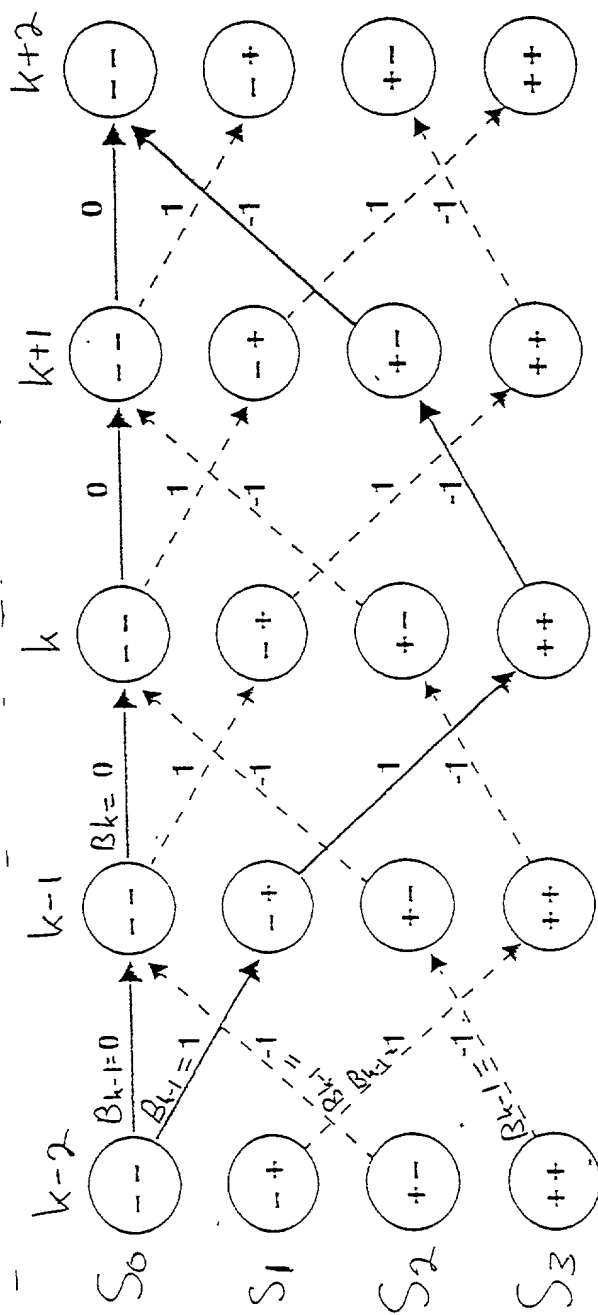


Figure 12

FIGURE 13





154a

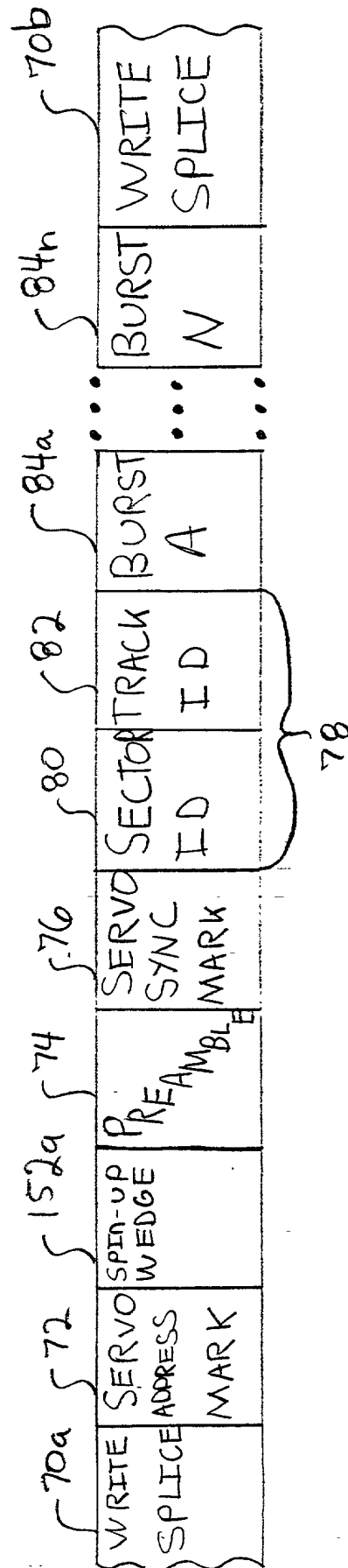


FIG. 16

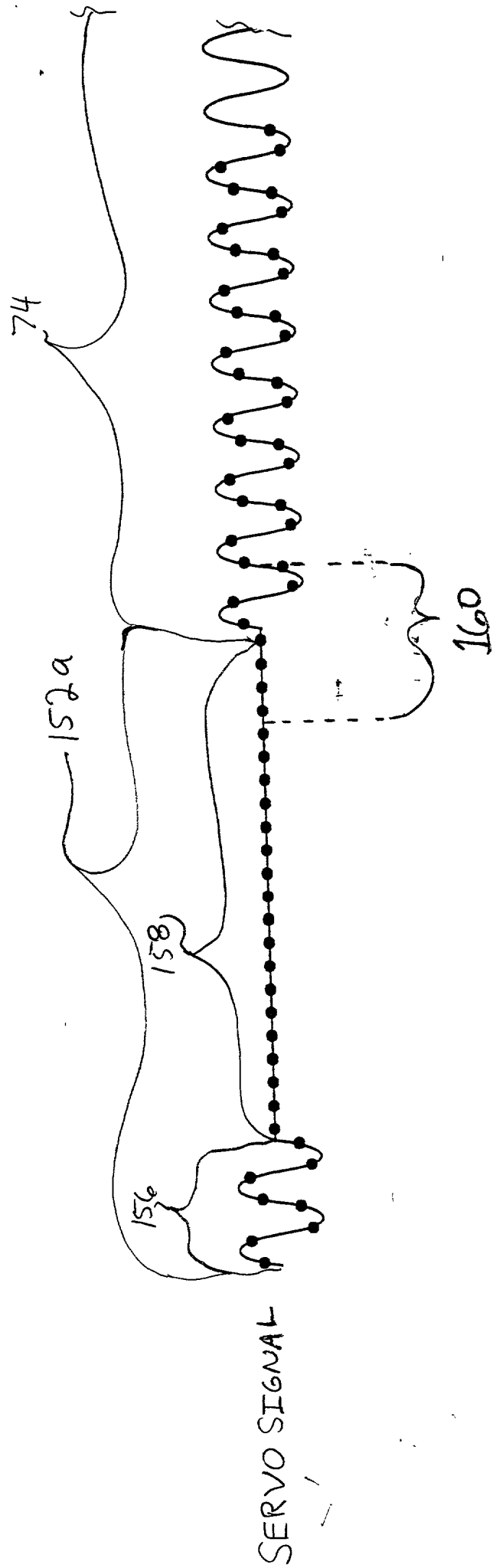


FIG. 17

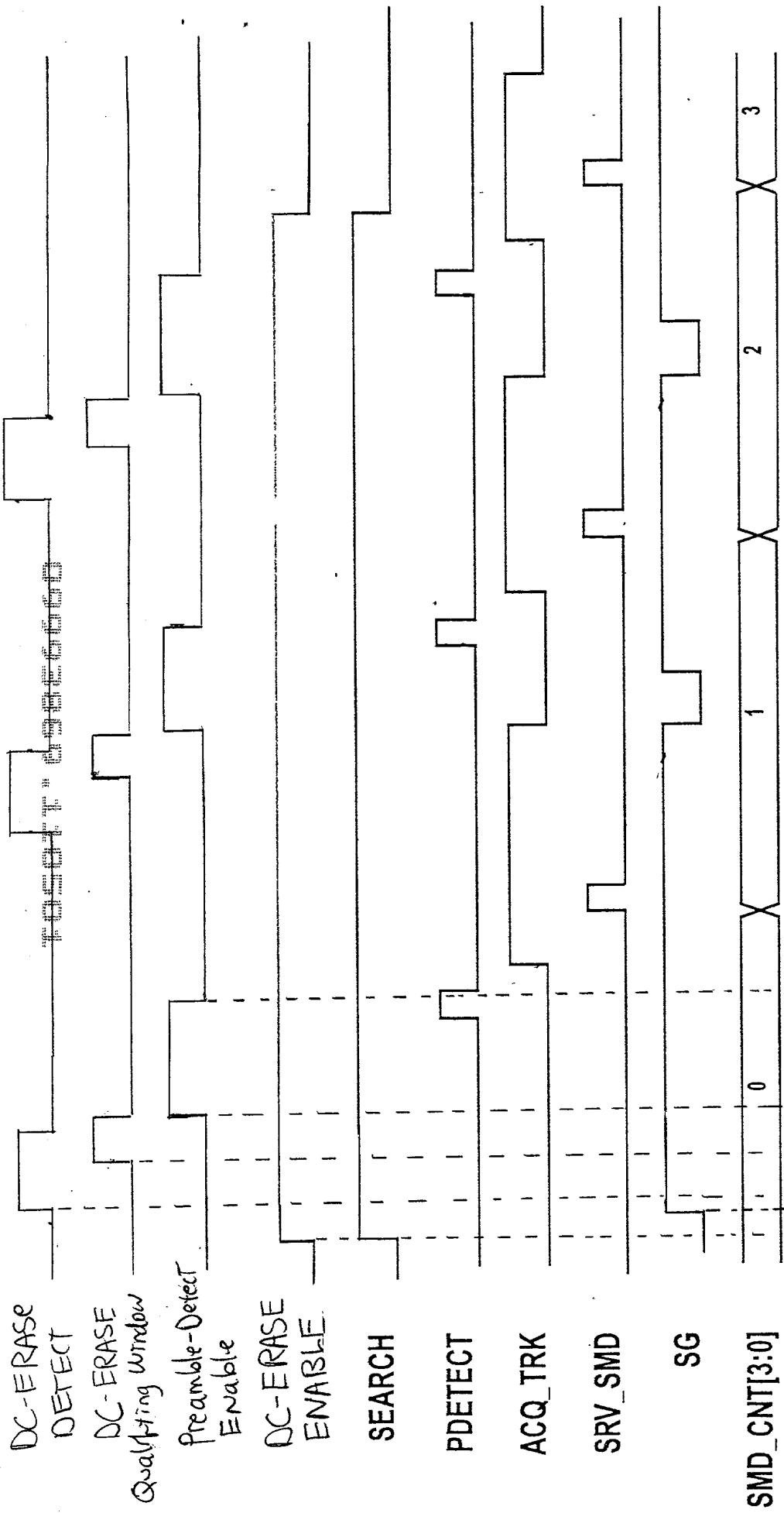


FIGURE 18

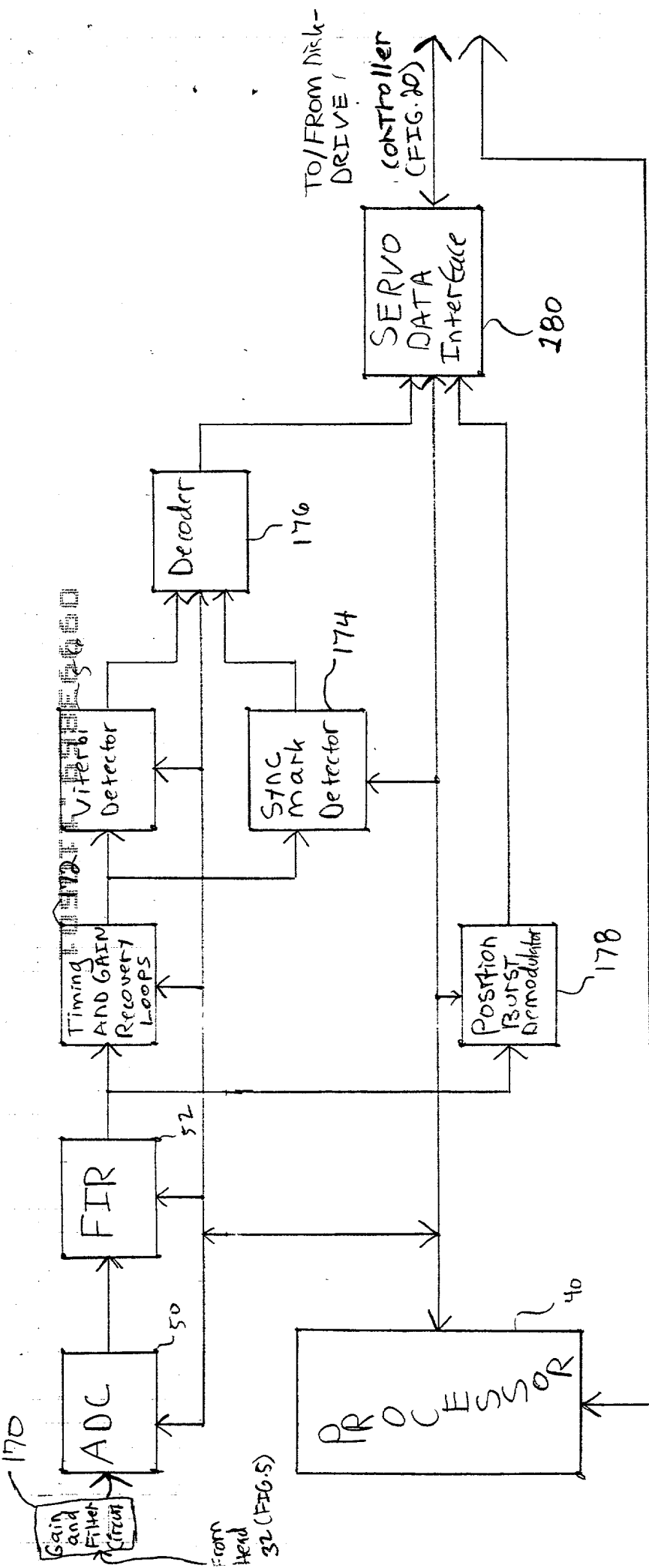


FIG. 19

30

FOOT" 6986660

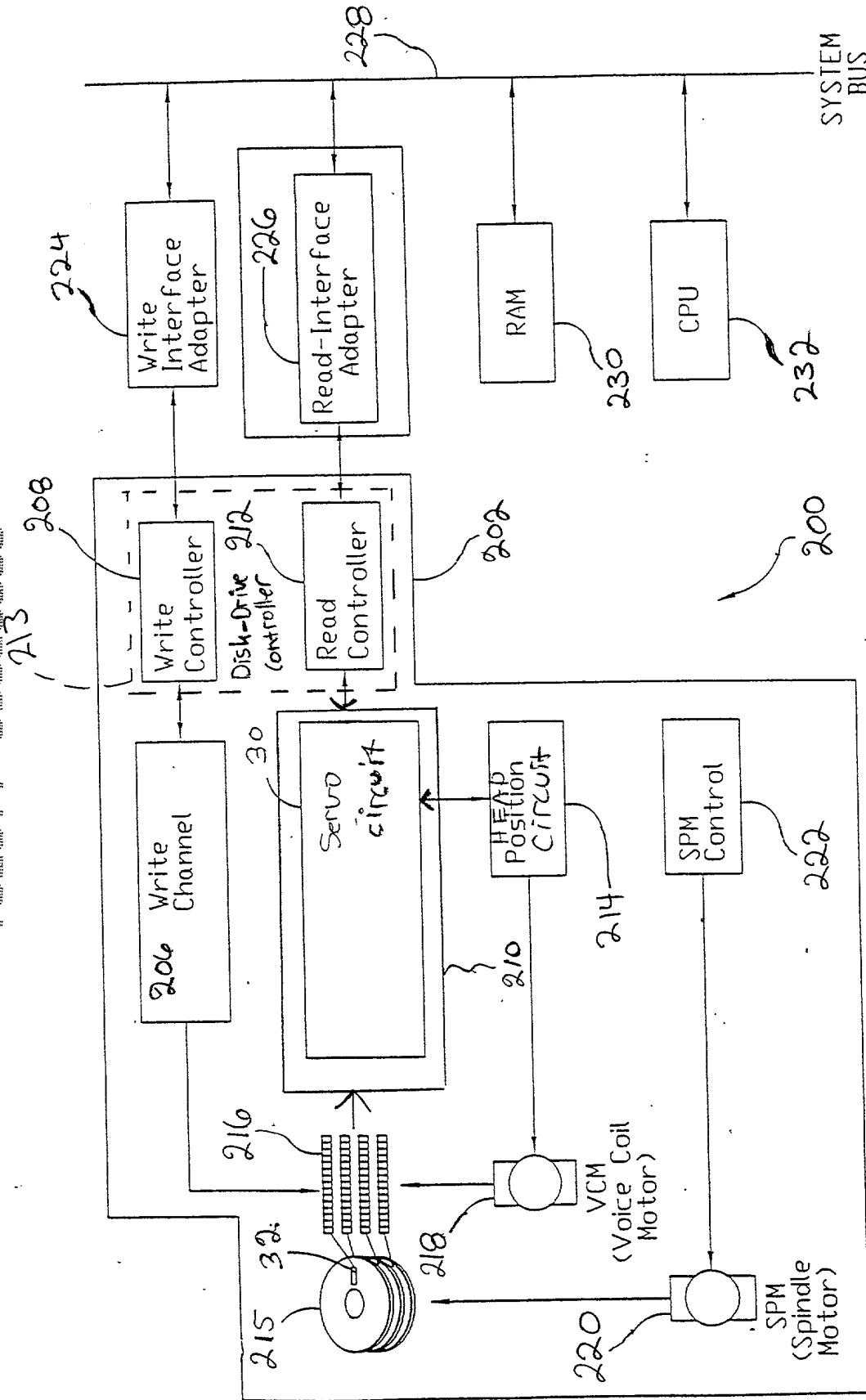


Fig. 20